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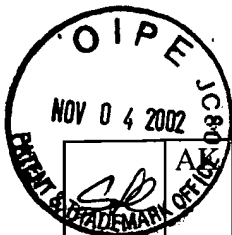
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				Application Number	09/782,886	
				Filing Date	February 13, 2001	
				First Named Inventor	Blais, Alexandre	
				Art Unit	2811	
				Examiner Name		
Sheet	1	of	4	Attorney Docket Number	11090-030-999	
U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
SA	AA	US -5,768,297	6/6/98	Peter W. Shor		
	AB	US -6,301,029	10/9/01	Hiroo Azuma		
	AC	US -6,317,766	11/13/01	Lov K. Grover		
	AD	US20010020701A1	9/13/01	Zagoskin		
	AE	US20010023943A1	9/27/01	Zagoskin		
FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
	AF					
	AG					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
SA	AH	E. Aarts and J. Korst, <i>Simulated Annealing and Boltzmann Machines</i> , pp. 12- 27 (Wiley, New York, 1989).				
	AI	D.S. Abrams and S. Lloyd, "Quantum Algorithm Providing Exponential Speed Increase for Finding Eigenvalues and Eigenvectors" <i>Physical Review Letters</i> 83, pp. 5162-5165 (1999).				
	AJ	A. Barenco, Charles H. Bennett, Richard Cleve, David P. Divencenzo, Norman Margolus, Peter Shor, Tycho Sleator, John A Smolin, and Harald Weinfurter, "Elementary gates for quantum computation", <i>Physical Review A</i> 52, 3457-3467 (1995).				

Examiner Signature	<i>Shourav Ghosh</i>	Date Considered	7/7/04
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AL	David Beckman, Amalavoyal N. Chari et al., "Efficient networks for quantum factoring", Los Alamos National Laboratory preprint quant-ph/9602016 (1996), accessed October 2, 2002.
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Examiner Signature	<i>Shoung-Hu</i>	Date Considered	7/2/04
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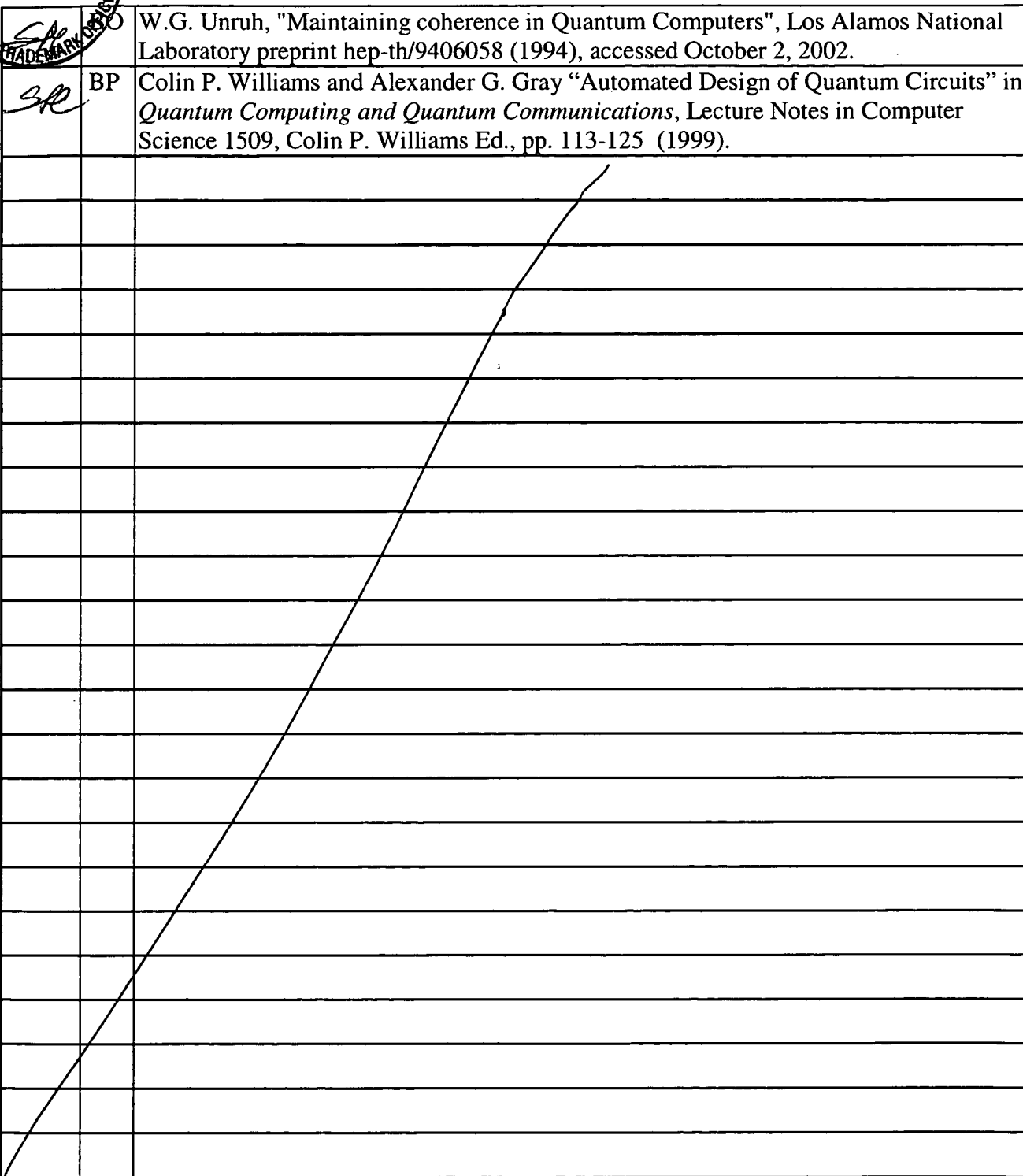
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BK	S.M. Sait and H. Youssef, <i>VLSI Physical Design Automation</i> , pp. 141-195 (IEEE Press, New York, 1995).
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Examiner Signature	<i>Sharon Megala</i>	Date Considered	2/7/04
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